

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10066182	FILING DATE 01/31/2002	CLASS 365	SUBCLASS 201	GAU 2818	EXAMINER PHAN
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**APPLICANTS: McClure David;

**CONTINUING DATA VERIFIED: NO

** FOREIGN APPLICATIONS VERIFIED: NO

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	ATTORNEY DOCKET NO 01-C-050
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiners's initials JP		<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	

TITLE : Circuit and method for testing a ferroelectric memory device

U.S. DEPT. OF COMM./PAT. & TM-PTO-436L(Rev. 12-94)

NOTICE OF ALLOWANCE MAILED

Assistant Examiner

ISSUE FEE

Amount Due	Date Paid
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TERMINAL

DISCLAIMER

NUMBER OF CLAIMS ALLOWED

Total Claims Print Claim for O.G.

FIGURE DRAWING NUMBER

Sheets Drawn Fig Drawing Print Fig

PREPARED FOR ISSUE

Application Examiner

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